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For

SYSTEM AND METHOD FOR PROCESSING CONTROL CELLS TO
PREVENT EVENT MISSEQUENCING AND DATA LOSS IN IMA
GROUPS

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**SYSTEM AND METHOD FOR PROCESSING CONTROL CELLS TO
PREVENT EVENT MISSEQUENCING AND DATA LOSS IN IMA
GROUPS**

FIELD OF THE INVENTION

[0001] This invention relates generally to data communications and, more particularly, to a system and method for processing control cells to prevent event missequencing and data loss in IMA groups.

BACKGROUND

[0002] Packet switching communication networks are commonly employed to transfer digital information over long distances. One example of a packet switching communication network is an Asynchronous Transfer Mode (ATM) communication network. An ATM communication network enables the transfer of digital voice information, digital video information and digital data over short or long distances via common carrier communication links. In one embodiment, the ATM communication networks enable a wide variety of communication devices to share common carrier communication links on a demand driven basis. For example, such ATM communication networks are commonly employed for a wide area network (WAN) communication.

[0003] In one embodiment, the common carrier communication links employed in such ATM communication networks include metal wire based communication links. One such type of common carrier communication link available in North America is the T1 communication link. In one embodiment, the T1 communication link provides a maximum data rate of 1.544 megabits

per second. Alternatively, an E1 communication link may be used as common carrier communication link. In one embodiment, the E1 communication link provides a maximum data rate of 2.048 megabits per second.

[0004] Figure 1A is a block diagram of one embodiment of a communication network. As illustrated in Figure 1A, communication network 100 includes communication nodes 120 and 130 coupled for communication using a set of communication links 110. In one embodiment, communication network 100 is an ATM communication network. Each communication node 120 and 130 is further coupled to other communication nodes. For example, as shown in Figure 1A, communication node 120 is coupled to node 122 via link 140 and to node 124 via link 142. In addition, communication node 130 is coupled to node 132 via link 144 and to node 134 via link 146.

[0005] In one embodiment, the ATM communication network 100 enables communications according to an Inverse Multiplexing Asynchronous Transfer Mode (IMA) communication protocol. The IMA communication protocol specifies a method of inverse multiplexing of data cells, for example ATM cells, over multiple communication links at the transmitting end and retrieving the ATM cell stream at the receiving end. In one embodiment, communication nodes 120 and 130 perform inverse multiplexing and de-multiplexing of data cells in a cyclical fashion among communication links 1-N grouped to form a higher bandwidth logical link 110 whose rate is approximately the sum of the rates of each communication link 1-N.

[0006] In one embodiment, at the near end, for example within transmitting communication node 120, the cell stream is distributed on a cell-by-cell basis,

across the multiple communication links 1-N. At the far end, the receiving communication node 130 recombines the cells from each link, on a cell-by-cell basis, recreating the original cell stream.

[0007] In addition to ATM cells, the IMA protocol provides for periodic transmission of control cells, defined as IMA Control Protocol (ICP) cells, which contain specific control information for the reconstruction of the ATM cell stream at the receiving end. In one embodiment, one ICP cell is transmitted on each communication link 1-N after a predetermined number of data cells.

[0008] In one embodiment, the communication links may have different transmission delays resulting in end-to-end differential delay among the communication links. The information contained within the ICP cells needs to be extracted and processed as soon as the ICP cells arrive at the receiving end. However, when differential delay is present, some ICP cells are delayed, and processing of non-compensated ICP cells at the time of arrival introduces missequencing of the information retrieved, which could lead to inaccurate reconstruction of the data stream and loss of ATM data cells.

[0009] Figure 1B is a block diagram of an example of a communication network. As illustrated in Figure 1B, two communication nodes 150 and 160 enable point-to-point IMA communication through communication links 170 and 180. In one embodiment, the communication nodes 150 and 160 are referred to as IMA groups.

[0010] In one embodiment, link 170 has a bi-directional delay of 270 milliseconds, which corresponds to 1000 ATM cells. Node 150 has buffered

1000 ATM cells transmitted on link 180, which is the fast link. Node 160 decides to delete link 180. A transmitting link state machine within node 160 transitions from ACTIVE mode to DELETED mode and filler cells are sent on link 180. At the same time, a receiving link state machine within node 160 also transitions from ACTIVE mode to DELETED mode.

[0011] Node 150 receives an ICP cell on both links 170 and 180 indicating the state of both links. Node 150 processes both ICP cells at the same time, without compensating for the delay existent on link 170. A transmitting state machine within node 150 transitions from ACTIVE mode to DELETED mode and filler cells are sent on link 180. At the same time, a receiving state machine within node 150 transitions from ACTIVE mode to USABLE mode and removes link 180 from use. As a result, the already buffered ATM cells are dropped.

SUMMARY OF INVENTION

[0012] A system and method for processing control cells to prevent event missequencing and data loss are described. At least one control unit is processed to retrieve communication information related to data received along one or more communication links in a network. The at least one control unit is then stored in one or more buffers. Finally, the at least one control unit is further processed to retrieve link information related to the one or more communication links.

[0013] Other features and advantages of the present invention will be apparent from the accompanying drawings and from the detailed description that follows.

BRIEF DESCRIPTION OF THE DRAWINGS

[0014] The present invention is illustrated by way of example and not limitation in the figures of the accompanying drawings, in which like references indicate similar elements and in which:

[0015] **Figure 1A** is a block diagram of one embodiment of a communication network.

[0016] **Figure 1B** is a block diagram of an example of a communication network.

[0017] **Figure 2** is a block diagram of one embodiment of a communication node within the communication network.

[0018] **Figure 3** is a block diagram of one embodiment of a delay compensation buffer module within the communication node.

[0019] **Figure 4** is a flow diagram of one embodiment of a method for processing control cells to prevent event missequencing and data loss.

DETAILED DESCRIPTION

[0020] According to embodiments described herein, a system and method for processing control cells to prevent event missequencing and data loss are described.

[0021] Figure 1A is a block diagram of one embodiment of a communication network. As illustrated in Figure 1A, the communication network 100 includes communication nodes 120 and 130 coupled for communication using a set of communication links 110. In one embodiment, communication network 100 enables communication according to an Inverse Multiplexing Asynchronous Transfer Mode (IMA) communication protocol.

[0022] Communication nodes 120 and 130 enable point-to-point IMA communication via the set of communication links 110. In one embodiment, each of the communication links 1-N within the set 110 is a T1 common carrier communication link. Alternatively, each communication link 1-N may be an E1 communication link.

[0023] In one embodiment, communication nodes 120 and 130 exchange streams of data, for example data units or communication data cells, along the set of communication links 110. The aggregate bandwidth of the set of communication links 110 provides point-to-point communications between communication nodes 120 and 130 at an effective data rate of N times the data rate of each of the individual communication links 1-N within the set 110. For example, in one embodiment wherein the set of communication links 110 includes a set of three T1 common carrier communication links, the maximum

effective data rate communication between communication nodes 120 and 130 is 3x1.544 megabits per second.

[0024] In one embodiment, communication node 120 transfers outbound sequences of communication data cells to communication node 130 by inverse multiplexing the outbound communication data cells over the set of communication links 110. Communication node 130 receives the inverse multiplexed inbound communication data cells from the communication node 120 over the communication links 1-N and reconstructs the original sequence of communication data cells. Communication node 130 compensates for delays associated with each of the communication links 1-N as described in further detail below.

[0025] Similarly, in an alternate embodiment, communication node 130 transmits inverse multiplexed outbound sequences of communication data cells to communication node 120 over the set of communication links 110. Communication node 120 receives the inverse multiplexed inbound communication data cells over the communication links 1-N and reconstructs the original sequence of communication data cells while compensating for the delays associated with transmission along each of the communication links 1-N.

[0026] Communication nodes 120 and 130 provide full duplex IMA communication via the set of communication links 110. For purposes of illustration, however, only transmission of communication data cells from transmitting communication node 120 to receiving communication node 130 is described in further detail below.

[0027] In one embodiment, the IMA protocol for the communication network specifies the method of inverse multiplexing of communication data cells over the set of multiple communication links 110. Also, the IMA protocol specifies the method of retrieving and multiplexing the data cell stream.

links 1 through N. Communication node 130 further includes a delay compensation buffer module 210 and a state machine 220 coupled to the buffer module 210. In one embodiment, buffer module 210 receives and stores the inbound communication data cells and the ICP cells transmitted along communication links 1-N. State machine 220 monitors link and group state information received along communication links 1-N. The delay compensation buffer module 210 will be described in further detail below.

[0033] **Figure 3** is a block diagram of one embodiment of a delay compensation buffer module within the communication node 130. As illustrated in Figure 3, buffer module 210 includes multiple data buffers 320 configured to receive communication cells transmitted along communication links 1-N within the set of communication links 110. In one embodiment, one data buffer 320 is provided for each communication link 1-N. In Figure 3, only three data buffers 320 are shown, corresponding to three communication links, respectively. However, it is to be understood that N buffers are provided to correspond to each communication link 1-N. Each data buffer 320 is configured to compensate for the delay existent on the corresponding communication link.

[0034] In one embodiment, buffer module 210 further includes a data processing module 340 coupled to the data buffers 320 for processing the stored data cells. Once processed, the data cells are stored in a sequence buffer 360 or passed to the ATM layer. In one embodiment, data cells are stored and processed in the original transmission sequence.

[0035] Buffer module 210 further includes multiple ICP communication processing modules 310 and an ICP state processing module 350. In one embodiment, one ICP communication processing module 310 is provided for each communication link 1-N. Each ICP communication processing module 310 is also coupled to one respective data buffer 320. Each ICP communication processing module 310 receives data cells and ICP cells along the respective communication link and transmits the data cells and ICP cells to the corresponding data buffer 320 after the ICP cells are processed. The ICP state processing module 350 is coupled to each data buffer 320 for further processing the ICP cells and for storing the ICP cells in an ICP buffer 370. In one embodiment, the ICP cells are stored in their original transmission sequence.

[0036] In one embodiment, each communication link of the set of communication links 110 incorporates a transmission delay resulting in a differential delay among the communication links. As the data cells and the ICP cells are received within buffer module 210, they are stored in the order they are received within data buffers 320.

[0037] In one embodiment, each ICP communication processing module 310 processes the ICP cells upon arrival to extract communication information, for example the frame delineation information and the delay information associated with the set of communication links 110. Using the frame delineation information and the delay information, a delay profile is constructed within the buffer module 210 to account for the differential delay along the set of communication links 110.

[0038] Once queued in the delay compensated data buffers 320, the data cells are not released until link and state machine information is also retrieved to provide a diagnosis of the communication links and of the state machines.

[0039] Subsequent to the processing of the ICP cells to extract the communication information, the ICP state processing module 350 transfers the ICP cells to the ICP buffer 370 and stores the ICP cells in their original transmission sequence. As the ICP cells pass through the delay compensation buffer module 210, the ICP state processing module 350 processes the ICP cells to extract the link and state machine information. Because the ICP cells are stored in their original sequence, the link and state machine information is delay compensated and it is obtained in the exact sequence transmitted by the transmitting communication node 120. The link and state machine information provides the status of the communication links and of the state machines within the network and prevents loss of the queued data cells.

[0040] Once the data cells are released from the data buffers 320, the data processing module 340 uses the delay information within the delay profile to process the data cells and order them in their original sequence of transmission. The ordered data cells are then stored in the sequence buffer 360 or passed to the ATM layer.

[0041] **Figure 4** is a flow diagram of one embodiment of a method for processing control cells to prevent event missequencing and data loss. As illustrated in Figure 4, at processing block 410, data cells and ICP cells are received within the buffer module 210.

[0042] At processing block 420, the ICP cells are processed to retrieve frame delineation information and delay information. In one embodiment, a delay profile is constructed based on the delay information retrieved.

[0043] At processing block 430, data cells and ICP cells are stored within delay compensated data buffers 32. Each data buffer 320 stores both data cells and ICP cells as they arrive along the set of communication links 110.

[0044] At processing block 440, the ICP cells are processed to retrieve link and state machine information and stored in an ICP buffer in their original sequence of transmission.

[0045] Finally, at processing block 450, data cells are processed and stored within a sequence buffer 360. In one embodiment, data cells are processed using the delay profile and are stored in their original transmission sequence within the sequence buffer 360. Alternatively, processed data cells are passed to the ATM layer.

[0046] It is to be understood that embodiments of this invention may be used as or to support software programs executed upon some form of processing core (such as the CPU of a computer) or otherwise implemented or realized upon or within a machine or computer readable medium. A machine readable medium includes any mechanism for storing or transmitting information in a form readable by a machine (e.g., a computer). For example, a machine readable medium includes read-only memory (ROM); random access memory (RAM); magnetic disk storage media; optical storage media; flash memory devices; electrical, optical, acoustical or other form of propagated

signals (e.g., carrier waves, infrared signals, digital signals, etc.); or any other type of media suitable for storing or transmitting information.

[0047] In the foregoing specification, the invention has been described with reference to specific exemplary embodiments thereof. It will, however, be evident that various modifications and changes may be made thereto without departing from the broader spirit and scope of the invention as set forth in the appended claims. The specification and drawings are, accordingly, to be regarded in an illustrative rather than a restrictive sense.